

**AMENDMENTS TO THE SPECIFICATION**

Replace the paragraph beginning on page 1, line 11, with the following:

A CONFIGURABLE ALL-DIGITAL COHERENT DEMODULATOR SYSTEM FOR SPREAD  
SPECTRUM APPLICATIONS

Serial No. To Be Assigned, Attorney Docket No. ~~9824 0037 999~~09/751,783

Replace the paragraph beginning on page 1, line 15, with the following:

A CONFIGURABLE MULTIMODE DESPREADER FOR SPREAD SPECTRUM  
APPLICATIONS

Serial No. To Be Assigned, Attorney Docket No. ~~9824 0036 999~~09/751,785

Replace the paragraph beginning on page 1, line 19, with the following:

APPARATUS AND METHOD FOR CALCULATING AND IMPLEMENTING A FIBONACCI  
MASK FOR A CODE GENERATOR

Serial No. To Be Assigned, Attorney Docket No. ~~9824 0032 999~~09/751,776

Replace the paragraph beginning on page 1, line 23, with the following:

A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM SPECTRUM  
APPLICATIONS

Serial No. ~~To Be Assigned, Attorney Docket No. 9824-029-999~~ 09/751,782, now Patent No.  
6,567,017

Replace the paragraph beginning on page 1, line 27, with the following:

METHOD AND APPARATUS TO SUPPORT MULTI STANDARD, MULTI SERVICE BASE-  
STATIONS FOR WIRELESS VOICE AND DATA NETWORKS

Serial No. ~~To Be Assigned, Attorney Docket No. 9824-035-999~~ 09/752,050

Replace the paragraph beginning on page 14, line 1, with the following:

In step 4004 of the present embodiment, parallel versions of a second code sequence are received. In the present embodiment, the second code sequence is the same sequence of bits as the first code sequence, but with an unknown relative phase offset from the first code sequence. Step 4004 is implemented in one embodiment by providing the second code sequence from a code generator, e.g., code generator 113 of Figure 1. In one embodiment, the code generator provides a single version of the second code sequence to the searcher, in which circuitry implements a unique offset for the parallel versions, as described in step 4006. In another embodiment, the code generator itself provides the unique phase offset in each of several code sequences respectively

provided to one each of the parallel versions of the second code sequence provided to the fast searcher, e.g., searcher 120c of Figure 2C. Co-pending US patent application serial number

          , 09/751,782, entitled "A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS" by Joel Medlock et al., ~~attorney docket number 9824-~~

~~0029-888~~now US patent number 6,567,017, provides additional detail on the configurable code

generator. This related application is commonly assigned, and is hereby incorporated by reference.

Alternatively, code sequences can be stored in memory for submission to a searcher.